

Series Editor
Jean-Charles Pomerol

Microprocessor 2

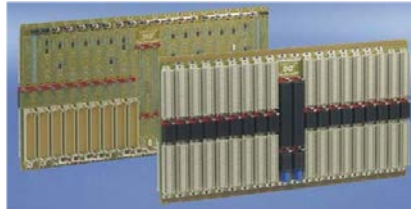
*Core Concepts —
Communication in a Digital System*

Philippe Darche

Color Section



a)



b)

Figure 1.7. *Ribbon-cable I/O bus (IDE) and a backplane bus*



Figure 4.19. *The SLI by Nvidia*



Figure 4.23. *A VME64x chassis with its backplane and a VPX 6U board*

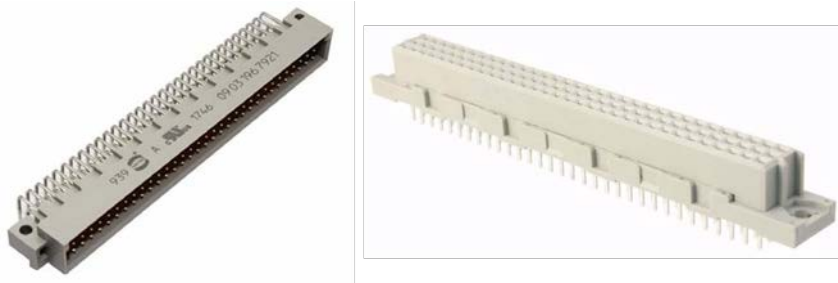


Figure 4.24. *Female and male connectors, standard DIN41612*